

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Joel PAGE, et al. ✓

Serial No.: 09/153,864 ✓

Filed: September 16, 1998 ✓

For: POWER ON RESET TECHNIQUES FOR AN INTEGRATED CIRCUIT CHIP ✓



Group Art Unit: 2816

Examiner:

Honorable Commissioner of
Patents and Trademarks
Washington, D. C. 20231

CORRESPONDENCE ADDRESS CHANGE

Sir:

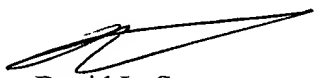
Please change the records to indicate the current firm name and telephone number for the above-identified application and forward all future correspondence as follows:

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Respectfully submitted,

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